



P.G.VARNA KUMAR REDDY , M.Tech.,(Ph.D)

Assistant Professor (Ad hoc),

JNTUA College of Engineering, Pulivendula

Pulivendula – 516390, Andhra Pradesh, India

Contact: +91-9666446766

Email : varnakumar.ece@jntua.ac.in

Date of Birth: 05/06/1989

Bio-data

Name : P.G.Varna Kumar Reddy

Official Address : Assistant Professor (Adhoc),
Department of Electronics and Communication Engineering,
JNTUA College of Engineering Pulivendula,
Pulivendula – 516390,
Andhra Pradesh ,INDIA.
Mobile :9666446766.

Residential Address : D.No:1/38,
Y.Kotha Palli(v),
Simhadripuram (M),
Pulivendula – 516390,
Andhra Pradesh (India)

E-mail : varnakumar.ece@jntua.ac.in

Nationality : Indian

Religion : Hindu

Educational Qualifications:

S. No.	Qualification	University/Institution	Specialization	Year of award
1	Ph.D (Pursuing)	Vels institute of science, Technology & Advanced studies	Wireless communication	Pursuing
2	M.Tech	JNTU Anantapur,	VLSI Design	2013
3	B.Tech	JNTU Anantapur	Electronics & Communication Engineering	2010

Professional Experience :

S.No.	Institution/Organization	Position held	Duration
1.	JNTUA College of Engineering, Pulivendula	Assistant Professor (adhoc) Department of ECE	2014 – till date

Administrative Experience

S.No.	Administrative Post held	Duration
1.	Coordinator for Public Relations	01-07-2019 To 15-03- 2022

Teaching Experience: 8+ years of teaching UG & PG Courses

Subjects & Labs Taught:

Undergraduate courses	Labs Handled
-----------------------	--------------

<ul style="list-style-type: none"> ❖ Linear IC Applications ❖ Electronic Devices & Circuits ❖ Switching Theory and Logic Design ❖ Analog Communications ❖ Digital Communications ❖ Electronic Circuits analysis & Design ❖ Network Theory ❖ Analog Electronic circuits ❖ Basic Electrical and Electronics Engineering ❖ Linear & Digital IC Applications 	<ul style="list-style-type: none"> ❖ Electronic Devices & Circuits Laboratory ❖ Basic Simulation Laboratory ❖ Analog Electronic Circuits Laboratory ❖ Digital Communications Laboratory ❖ Electronic Circuits analysis & Design Laboratory ❖ Basic Electrical and Electronics Engineering Laboratory ❖ Analog Communications Laboratory ❖ Digital Logic Design Laboratory ❖ Network Theory Laboratory ❖ Linear & Digital IC Applications Laboratory
--	---

Number of Research publications:

International journals:	10
International Conferences:	04
National Conferences:	02

List of publications:

International Journals:

- ❖ **P.G.Varna Kumar Reddy, Dr.M.Meena** Published a paper on “**Automatic modulation classification using a support vector machine based pattern recognition algorithm**” was published in Ingenierie des systemes d’information(**Scopus indexed**) Volume 27,No.6,December 2022,PP.999-1007.
- ❖ **P.G.Varna Kumar Reddy, Dr.M.Meena** Published a paper on “**Machine learning based automatic modulation classification for future wireless communication**” was published in High Technology letters(**Scopus indexed**) Vol 27 Issue 11 december 21 ISSN No: 1006-6748
- ❖ **P.G.Varna Kumar Reddy,G.Muni Venkatesh,K.Swarna Latha,Y.Narendra Reddy,T.Vasaiah** Published a paper on “**Simulation and analysis of spectrum sensing in cognitive radio** “has been published in International journal of Electronics,communication and Instrumentation Engineering research and development(IJECIERD) ISSN(P):2249-684X;ISSN(E):2249-7951,Vol 11,Issue 02 Dec 20-21.
- ❖ **P.G. Varna Kumar Reddy K.Ravindra Reddy.K,Nageswara Rao,G.Krishna Murthy** Published a Paper on “**Video text steganography by using discrete wavelet transform**” has been published in International journal for research in engineering application and management(IJREAM),volume:07,Issue 05,August 2021(UGC Approved).

- ❖ **P.G.Varna Kumar Reddy, Shaik Taj Mahaboob, P. Jayanthi**, Published a paper on “**Tracking of foreground objects using FCNN classifier**” has been published in Journal of Engineering sciences vol 11 issue 1 january 2020 Issn:0377-9247.
- ❖ **P.G.Varna Kumar Reddy, Shaik Taj Mahaboob, P. Jayanthi**, Published a paper on “**A Comprehensive study of visual tracking via non local similarity learning**” has been published in journal of emerging technologies and innovative research Vol:6 issue 3 march 2019, ISSN:2349-5162.
- ❖ **P.G. Varna Kumar Reddy, K. Ravindra Reddy, K. Siva Chandra**, published a Paper on “**An Improved Histogram Modification Based on Reversible Data Hiding Algorithm**” in the international journal of Engineering research and application, ISSN : 2248-9622, Vol. 8, Issue 1, (Part -V) January 2018, pp.61-67
- ❖ **P.G. Varna Kumar Reddy, K. Ravindra Reddy, K. Siva Chandra, T.S. Mahammed Basha**, Published a paper on “**Small-Cell Zooming Scheme for Energy Efficient Two-Tier Hybrid Green Cellular Networks**” in “International journal of engineering and techniques special issue ISSN : 2395 - 1303 March 2018.
- ❖ **P.G.Varna Kumar Reddy, M. Siva Kumar, P. Naga Mallaiah**, published Paper on “**Design and Implementation of a PCI Express Physical Layer Transmit Protocol**” was published in international journal of Engineering research on-line, ISSN: 2321-7758, Vol 4. Issue 3 , May-June 2016.
- ❖ **P.G.Varna Kumar Reddy, K.K. Varalakshmi, P. Satya Srikanth**, published Paper on “**MIMO Wireless Systems for Power Management**” was published in international journal of Applied Sciences, Engineering and Management Vol 2. ISSN 2320-3439 , September 2013.

International Conferences:

- ❖ Participated in 3rd congress on Intelligent systems CIS 2022 organised by Christ (deemed to be University) Bengaluru technically sponsored by Soft computing research society held online during september 05-06,2022, ISBN 978-981-19-9379-4 ,https://doi.org/10.1007/978-981-19-9379-4_21, Paper Title : **Adaptive Modulation Classification with deep learning for next generation communication.**
- ❖ Participated in International conference on Recent trends in Engineering and Technology(ICRTET-2022) organised by KSRM College of Engineering (UGC-Autonomous),kadapa,Andhrapradesh,India held on 29th December 2022.Paper Title: **Satellite Image Segmentation.**
- ❖ Participated in International conference on Future Communication and Internet of Things (ICFCIoT 2019)-ISBN No: 978-93-5351-061-9 Organized by Aditya College of Engineering during 15th -16th March 2019 at Madanapalle Andhrapradesh India. Paper Title: **A Literature review on Visual Tracking with weighted adaptive local sparse appearance model via spatio- temporal context learning.**
- ❖ Participated in International conference on Communication and electronics systems (ICCES) organized by PPG Institute of technology during 15-16 october 2018 at Coimbatore tamil Nadu

INDIA. Paper Title: **Improvement of colour images of to a great degree low light scenes.** will be published in IEEE xplore ISBN: 978-1-5386-4765-3

National Conferences:

- ❖ Participated in National conference on sustainable electronics and applications (NC'SEA-21) on 21st August 2021 organized by Department of Electronics and Communication Engineering, Annamacharya Institute of Technology and Sciences, Rajampet. Paper name: **Video Text Steganography by using Discrete Wavelet Transform.**
- ❖ K.Ravindra Reddy, K. Siva Chandra, T.S. MahammedBasha, P.G. Varna Kumar Reddy Presented a paper on “**Small-Cell Zooming Scheme for Energy Efficient Two-Tier Hybrid Green Cellular Networks**” in “National conference on Emerging Trends in Information, Management and Engineering Sciences (NC'e-TIMES # 1.0)” organized by Annamacharya Institute of Technology and Sciences, Rajampet, Kadapa(Dt), on 14th & 15th March 2018.

Workshops attended:

- ❖ Participated a five day online FDP on “**conitive computing**” organized by Department of ECE ,vels institute of science ,Technology &Advanced studies,chennai, during 20-03-2023 to 24-03-2023.
- ❖ Participated a five day online FDP on “**Design and implementation of advanced vlsi architectures**” organized by Department of ECE ,Lakireddy bali reddy college of engineering,Mylavaram, during 13-03-2023 to 17-03-2023.
- ❖ Participated a One day online FDP on“21st century teaching methodologies” organized by Department of ECE,Indira institute of technology and sciences, Markapur on 18-03-2023
- ❖ Participated a three day National level online FDP on “ **Research topics in VLSI and Industry trends**” organized by Department of ECE ,GMR Institute of technonology ,Rajam during 29th to 31st May 2020.
- ❖ Participated a one week faculty development programme on “**Digital Image Processing with MATLAB**” from 08th to 13th June 2020 conducted by Data Teach labs.
- ❖ Participated a One day Webinar on “**Expanding scope of robotics**” held on 21st May 2020 Organized by Sri Eshwar college of Engineering, Coimbatore.
- ❖ Participated a five day online faculty development programme on “**Research Trends in Image Processing**” held during 15th -19th June 2020, organized by Department of ECE,Gates institute of technology,Gooty.

- ❖ Participated a Two week faculty development programme on “**MAT LAB Tools and their application in Engineering**” organized by Data Teach Labs during 25th May 2020 -7th June 2020.
- ❖ Participated one day webinar on “**Research paperwriting and citation Manager**” organized by department of CSE ,PES college of Engineering,Aurangabad held on 21st June 2020.
- ❖ Participated in one day faculty development programme on “**Expanding scope of robotics**” organized by Sri Eshwar college of Engineering ,Coimbatore,Tamilnadu on 21st May 2020.
- ❖ Participated a five day AICTE Training and Learning (ATAL) academy, Gawahathi faculty development programme on “**Artificial Intelligence**” from 02nd to 06th December 2019 at JNTUA College of engineering Pulivendula.
- ❖ Attended a Five day FDP on “**Internet of Things(IOT) Advanced Level**” held at JNTUA College of Engineering Pulivendula during 6th -10th March 2017.
- ❖ Attended a Five day FDP on “**Spectrum Sharing in Next Generation Wireless Networks: Principles, Analysis and Case Studies**” held at IIT Hyderabad during 5th -9th September 2016.
- ❖ Attended a Two day FDP on “**Digital Image Processing, pattern recognition and its Applications**” held at JNTUA College of Engineering Pulivendula during 19th -20th September 2016.
- ❖ Attended a Two day FDP on “**VLSI Design Advancements using Xilinx Tools**” held at JNTUA College of Engineering Pulivendula during 26th -27th August 2016.
- ❖ Attended a Two day FDP on “**MSP 430 and TIVA Micro controllers**” held at JNTUA College of Engineering Pulivendula during 23rd -24th July 2016
- ❖ Attended a Two day FDP on “**Intreprenurship Development Initiative in India**” held at JNTUA College of Engineering Pulivendula during 16th -17th March 2016.
- ❖ Attended a Two day FDP on “**Linear Integrated Circuits: A Systems Approach**” held at JNTUA College of Engineering Pulivendula during 25th -26th September 2015.
- ❖ Participated in Two days hands on workshop on “**Linear Integrated Circuits: A System Approach**” organized by department of ECE JNTUA College of Engineering Pulivendula during 25th & 26th September 2015.

Certification :

- ❖ Successfully completed the **12 Week NPTEL Online certification** course on **Analog Communications** With consolidated score of 58%.

Declaration:

I hereby declare that the information furnished above is true to the best of knowledge

(P.G.VARNA KUMAR REDDY)

Date:14-06-2023
Pulivendula