



S.VARALAKSHMI, M.Tech.,(P.h.d)

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Bio-data

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Nationality : Indian

Religion : Hindu

Educational Qualifications:

S. No.	Qualification	University/Institution	Specialization	Year of award
1.	P.h.d	Vels University,Chennai	Machine Learning	On going
2.	M.Tech	JNTU Anantapur	Electronics & Communication Engineering	2015
3.	B.Tech	S.V.U Tirupathi.	Electronics & Communication Engineering	2011

Professional Experience :

S.No.	Institution/Organization	Position held	Duration
1.	JNTUA College of Engineering, Pulivendula	Assistant Professor (adhoc) Department of ECE	2016 – till date

Teaching Experience: 6+ years of teaching UG Courses

Subjects Taught:

Undergraduate courses	Post graduate courses
<ul style="list-style-type: none">❖ Electronic devices and circuits❖ Switching theory and logic design❖ Micro processors and micro controllers❖ Fundamentals of communication systems❖ Basic Electronics Engineering❖ Artificial intelligence and neural networks❖ Semi conductor devices and circuits	

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Number of Research publications:

International journals:	04
International Conferences:	03
National Conferences:	00

List of publications:

International Journals:

- S.VARALAKSHMI published Paper on “Use of memories and programmable logic arrays for asynchronous CAM ” was published in international journal of Engineering research and applications, Vol 5. Issue 16 , JUNE 2018,UGC Approved, Impact Factor: 5.60.
- S.VARALAKSHMI published paper on “**An Improved Low –Voltage Low Power Fully-Differential Double Folded Cascade Class AB Opamp for Pipeline ADC Applications**” *International Journal of Research in Electronics and Computer Engineering (IJRECE)*,E-ISSN: 2348 – 2281 & 2393-9028(PRINT),Volume 7, Issue2, April-June 2019,Page No: 763-768,UGC Approved, Impact Factor: 4.305.
- S.VARALAKSHMI published paper on “Design of IoT Network using Deep Learning-based Model for Anomaly Detection” in IEEE, December 2021.
- S.VARALAKSHMI published paper on “Deep CNN based multi classification of respiratory disease using X-ray images” in IEEE, January 2023.

International Conferences:

- Presented in International conference on (ICRCSET-2020) on 28-29 FEB 2020 organized by Chalapathy institute of technology, Guntur, Andhra Pradesh. Paper Name: Design of dual band L-slot micro strip patch antenna for wireless communications.

- Presented in International Conference on I-SMAC (IoT in Social, Mobile, Analytics and Cloud) University of Malaya on Design of IoT Network using Deep Learning-based Model for Anomaly Detection from 11-11-2021 to 13-11-2021.
- Presented in International Conference on ICPECTS-2022, Sri sai ram engg college, chennai on Deep CNN based multi classification of respiratory disease using X-ray images 8-12-2022 & 9-12-2022.

Workshops attended:

- Attended a two days workshop on “Digital Image Processing and pattern recognition and its applications” Organized by Dept of Electronics & Communication Engineering JNTUACEP, Pulivendula during 19-20 September 2016.
- Attended a two days workshop on “VLSI Design Advancements using Xilinx Tools” Organized by Dept of Electronics & Communication Engineering JNTUACEP, Pulivendula during 26-27 August 2016.
- Attended a ten days workshop on “Embedded Real Time Systems” Organized by E&ICT Academy, NIT, Warangal at Dept. of ECE JNTUA College of Engineering Ananthapuram, during 1 -10 Feb 2017.
- Attended a Five days workshop on “Internet of Things (IOT) Advanced Level” Organized by Dept of Electronics & Communication Engineering JNTUACEP, Pulivendula during 6-10 March 2017.
- Attended a One day workshop on “NPTEL WORKSHOP “ conducted on 15-12-2018 by IIT MADRAS at JNTUACEP.
- Attended a Two day workshop on “Academic challenges in 21st century” by sai engg college, Rayachoti from 20-05-2020 to 21-05-2020.
- Attended a Ten days online FDP on “Industrial embedded systems & IOT with Raayan Mini Board based on ARM cortex M4(STM 32)” 8-06-2020 to 19-06-2020.
- Attended a Three days fdp on “Machine learning with deployment” organized by Siddartha engg college, Ibrahim patnam from 3-07-20 to 5-07-2020.
- Attended a four day workshop on “Impact Of Covid – 19 On Indian Economy” Organized By The Department Of Business Administration from 31-07-2020 to 03-07-2020.
- Attended One Week Faculty Development Program on “PYTHON PROGRAMMING II” Organized by Department of Computer Science & Engineering, Anantha Lakshmi Institute of Technology & Sciences, Ananthapuram, in association with SPOKEN TUTORIAL IIT, MUMBAI from 03/08/2020 to 08/08/20.
- Attended six day workshop on “Quantum computing and Machine learning” by JNTUA Ananthapuram from 30-11-2020 to 5-12-2020.
- Attended five day workshop on Materials for photonic applications” by Mahatma Gandhi Institute of Technology from 9-8-2021 to 13-8-2021.
- Attended one week workshop on “Practical exposure on Machine learning and data science” by G. Pulla Reddy Engineering college from 13-09-2021 to 18-09-2021.

- Attended five days workshop on “Effective pedagogy for science” by JMJ College for women, Tenali from 29-11-2022 to 03-12-2022.

Declaration

I hereby declare that the information furnished above is true to the best of knowledge

(S.VARALAKSHMI)