G.KRISHNA MURTHY , M.Tech.

Assistant Professor(adhoc)

JNTUA College of Engineering,Pulivendula

Pulivendula – 516390, Andhra Pradesh, India

Bio-data

Name	:	G.KRISHNA MURTHY
Official Address	:	Assistant Professor(adhoc),
		Department of ECE,
		JNTUACEP,
		Pulivendula – 516390,
		Andhra Pradesh ,INDIA.
		Mobile :9133706929.
Residential Address	:	D.No:4-2-162,
		Madhuri ice factory street,
		Pulivendula – 516390,
		Andhra Pradesh (India)
E-mail	:	gkmurthy.ece@jntua.ac.in

Nationality : Indian

Religion : Hindu

Educational Qualifications:

S. No.	Qualification	University/Institution	Specialization	Year of award
1.	M.Tech	JNTU Anantapur	Digital Electronics and Communication Systems	2014
3.	B.Tech	JNTU Anantapur	Electronics & Communication Engineering	2011

Professional Experience :

S.No.	Institution/Organization	Position held	Duration
1.	JNTUA College of Engineering, Pulivendula	Assistant Professor (adhoc) Department of ECE	2014 – till date

Teaching Experience: 8+ years of teaching UG & PG Courses

Subjects Taught:

Undergraduate courses	Post graduate courses
❖ Microwave Engineering ❖ Electromagnetic waves and transmission lines ❖ Probability theory and stochastic processes ❖ Linear IC Applications ❖ Digital IC Applications ❖ Basic Electronics Engineering ❖ Satellite Communications	❖ Embedded systems ❖ Mobile communications

Number of Research publications:

International journals: 07
International Conferences: 01

List of publications:

International Journals:

➢ Pubished the paper titled"**Automatic modulation classification using machine learning**" in **Mukt Shabd** Journal, **Volume XII, Issue VI, JUNE/2023 , ISSN NO : 2347-3150.**

➢ Pubished the paper titled"**Stock price prediction using lstm, support vector machine, random forest and GRU**" in Industrial Engineering Journal : ISSN 0970-2555 Volume : 52, Issue 6, No. 6, June : 2023 UGC Care Approved, Group I, Peer Reviewed Journal with IF=6.82 .

➢ Pubished the paper titled**"System for preventing accidents in deadly and hilly areas**" In IJNRD (International Journal of Novel Research and Development) ,ISSN Approved & 8.76 Impact Factor ,Volume 8 Issue 7, July-2023

➢ Pubished the paper titled"**videotext steganography by using discrete wavelet transform**   " in International Journal of Research in Engineering application and management(IJREAM), Volume..7..Issue..5..,.-August 2021 (UGC approved)

➢ Pubished the paper titled "**An Improved Low-Voltage Low Power Fully-Differential Double Folded Cascode Class AB Opamp for Pipeline ADC Applications** " in International Journal of Research in Electronics and Communication Engineering, Volume..7..Issue..2..,.-May-june 2019 ,pp..763.-768(UGC approved)

➢ Pubished the paper titled "**Smart attendance system using IOT** " in International Journal of Research in journal of Engineering sciences, Volume..9..Issue..1, 2018 ,pp..61.-64, ISSN NO:0377-9254.(Scopus)

➢ Pubished the paper titled "**Wireless Weather Monitor Using IoT**" in i-manager journal on Embedded systems, Volume..6.No.1. Jan 2018 (UGC approved free journal)

International Conferences:

➢ participated in National conference on Cognitive & Intelligent Computinh (ICCIC-21) on 7th December 2021 organized by Department of Computer Science and Engineering,CMR college of Engineering & Technology,Hyderabad. Paper Name: Methodologies in Steganography and cryptography-Review.

Workshops attended:

➢ Participated in a five day national level online FDP on "**Congnitive Computing"** organized by Department of ECE, VELS Institute of science and technology,in associate with IETE ,Chennai during 20/03/2023 to 24/03/2023.

- Participated in a five day national level online FDP on "**Design and implementation of VLSI architectures**" organized by Department of ECE, Lakireddy Balireddy College of Engineering ,Mylavaram,Andhra Pradesh ,during 13/03/2023 to 17/03/2023.

- Participated in AICTE sponsred one week online short-Term training program on "**Practical Exposure on Machine Learning and Data Science**" organized by Department of ECE,GPREC(A),Kurnool during 13th september to 18th september 2021.

- Participated in a one week FDP on "**PYTHON PROGRAMMING**" organized by Department of CSE, Anantha Lakshmi Institute of Technology and Sciences,Ananthapuram in association with SPOKEN TUTORIAL IIT,MUMBAI,during 03/08/2020 to 08/08/2020.

- Participated in a five day national level online FDP on "**Trends in Electronics and communication Engineering**" organized by Department of ECE, Sri Vasavi Engineering college ,Tadepalligudem ,Andhra Pradesh.

- Participated in a five day national level online FDP on "**Research Trends in Image Processing**" organized by Department of ECE, GATES INSTITUTE OF TECHNOLOGY , during 15/06/2020 to 19/06/2020.

- Completed Two week FDP on **"Matlab Tools and their Application in Engineering"** from 25/05/2020 to 07/06/2020 conducted by Data Teach Labs

- Attended for three days National level hands on training programme o**n "Embedded Systems design using TIVA "** held in JNTU college at Kalikiri. (from 31 may- 2nd june 2018)

- Attended for Five day FDP on **"Internet of Things(IOT) Advanced Level "** held in JNTUACEP college at Pulivendula (6-10th –March 2017)

- Attended for Two day National workshop on **"VLSI Design Advancements using Xilinx Tools"** held in JNTUACEP college at Pulivendula (26,27th –August 2016).

- Attended for Two day National level hands-on training program on **"MSP 430 and TIVA Microcontrollers"** held in JNTUACEP college at Pulivendula (23,24th –July 2016)

- Attended for one day FDP on **"Patent drafting and filling "** held in JNTUACEP college at Pulivendula (23th -April2016)

- Attended for two day FDP on **"Enterpreneurship development initiative in India"** held in JNTUACEP college at Pulivendula (from 16-17th March 2016)

- Attended for three day National level hands on training programme on **"signal and image processing: practice to research"** held in VIT university ,vellore (from 25-27th February 2016) .

- Attended for two day National level hands on training programme on **"Lnear integrated circuits:A systems approach "** held in JNTUACEP college at Pulivendula (from 26-27th Sept 2015)

- Attended for five day national workshop on **" Network Simulation Tools**" at Sreenidhi Institute of science &Technology , Hyderabad(from 08-12th December 2014)

➢ Attended for two day National level hands on training programme on **"EMBEDDED SYSTEMS"** held in JNTUACEP college at Pulivendula (from 16-17th March 2012)

Declaration:

I hereby declare that the information furnished above is true to the best of knowledge

(G.KRISHNA MURTHY)