



Mrs. B.V.SOWJANYA , M.Tech.(Ph.D)
Assistant Professor (Adhoc) of ECE
Jawaharlal Nehru Technological University Anantapur College
of Engineering Pulivendula– 516 390, Andhra Pradesh, India.

Bio-data

Name : B.V.SOWJANYA
Official Address : Assistant Professor (adhoc), Department
of ECE,
JNTUACEP,
Pulivendula – 516390,
Andhra Pradesh (India).
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Pulivendula – 516390,
Andhra Pradesh (India).

E-mail : sowji.bv@gmail.com
Nationality : Indian
Religion : Hindu

Educational Qualifications:

S. No.	Qualification	University/Institution	Specialization	Year of award
1	Ph.D	Dr. A.P.J. Abdul Kalam University, Indore, Madhya Pradesh	Electronics & Communication Engineering	pursuing
2.	M.Tech	JNTU Anantapur	Digital Electronics and Communication Systems	2014
3.	B.Tech	Kuppam engineering college , kuppam.	Electronics & Communication Engineering	2011

Professional Experience :

S.No.	Institution/Organization	Position held	Duration
1.	JNTUA College of Engineering, Pulivendula	Assistant Professor (adhoc) Department of ECE	2015 – till date

Teaching Experience: 7+ years of teaching UG Courses

Subjects Taught:

Undergraduate courses	Labs Handled
<ul style="list-style-type: none">❖ Basic Electrical & Electronics Engineering❖ Digital Systems Design❖ Digital Communication systems❖ Electronic Measurements & Instrumentation❖ Microprocessor & Microcontroller❖ Optical fiber Communication❖ Analog Communication systems❖ Computer architecture and organization❖ Digital image processing❖ Wireless Communication❖ Design thinking for innovation	<ul style="list-style-type: none">❖ Electronic Devices & Circuits Laboratory❖ Digital Communications Laboratory❖ Basic Electrical and Electronics Engineering Laboratory❖ Analog Communications Laboratory❖ Microprocessor & Microcontroller Lab❖ Pulse and Digital Circuits Lab❖ Electronic Measurements & Instrumentation Lab

Additional Duties done:

- Department UG Project Coordinator
- I B.Tech CRC Chairman
- Department correspondence
- NBA/NAAC

Patents Published:

- Patent Application Number: 202041029576
- A Date of Filing of Application: 12-07-2020
- Date of Publication of the Patent: 31-07-2020
- Title of the Invention: Smart Parking and License Plate Detection

Certification :

- Successfully completed the **12 Week NPTEL Online certification** course on **microprocessor and micro controller** With consolidated score of 55%.

Number of Research publications:

International journals:	07
National journals:	01
International Conferences:	02

List of publications:

International Journals: 08

- B.V Sowjanya, Dr. Amol Kumbhare Published the paper titled " A Multi Resolution Wavelet Analysis System for recognizing Medical Images using Local Contrast Hole-Filling Algorithm" in international Journal of Engineering a UGC approved journal with ISSN: 0377-9254, Volume 13 Issue 07, July 2022, PP:-559-564.
- B.V Sowjanya, Dr. Amol Kumbhare Published the paper titled " **Communications and Image Processing**" in Design Engineering (DE) a Scopus journal with ISSN: 0011-9342, Issue 01, 2022, PP:-2836-2839, June 2022.
- N. Shehanaz, B.V Sowjanya Published the paper titled " **An High Speed Rationed CMOS Circuit Design With Modified Pseudo Logic Circuits**" in International Journal of Technical Innovation in Modern Engineering & Science (IJTIMES) an UGC approved journal with ISSN: 2455-2585, Volume 5, Special Issue 05, May-2019, PP:-49-53 with Impact Factor 5.22.
- N. Shehanaz, B.V Sowjanya, T.C Sanjeeva Rayudu Published the paper titled "**An Efficient and Improved Radix-16 Based Booth Multiplier for 64-Bit Based on the Reduced Height of Partial Product Array**" in Journal of Emerging Technologies and Innovative Research (JETIR), an UGC approved journal with ISSN: 2349-5162, Volume 5, Issue 5, May 2018, PP:174-178 with Impact Factor 5.87.
- N.merrin prasanna kumar, B.v.sowjanya Published the paper titled "**design and analysis of UWB antenna for wireless commercial applications**" Was published in International journal of applied science, engineering and management .ISSN:2320-3439, vol:5,issue:3, may 2016, PP:56-59.
- B.v.sowjanya, N.merrin prasanna Published the paper titled "**An efficient high performance wallace tree multiplier using modified booth encoder design and implementation**" Was published in International journal of applied science, engineering and management .ISSN:2320-3439, vol:5,issue:3, may 2016, PP:49-51.
- B.v.sowjanya Published the paper titled "**Moving message in GSM based low power E-notice display**" was publication in Global Journal of Trends in Engineering (GJTE). e-ISSN:2393-9923 ,volume(2), Issue(4), April 2015, pp:434-439.

International Conferences: 02

- Presented a paper on "Communication and Image Processing" at International multidisciplinary conference on innovative and current trends of research (IMCICTR-22)" organized by Department of Electrical and Electronics Engineering at Management studies, Kanpur, held on 18th September, 2022.

- Presented a paper on “A High Speed Rationed CMOS Circuit Design with Modified Pseudo Logic Circuits” at National Conference on Innovative Technologies in Electrical Engineering(NCITEE)-2k19organized by Department of Electrical and Electronics Engineering at Geetanjali Institute of Science and Technology, Nellore held on 30th March 2019.

FDP's/Workshops/Seminars attended

- Five days National online FDP on “Intellectual Property Rights” Organized by MGRERI, Tamilnadu during 22nd to 26th May 2023.
- One week IEEE BS Sponsored online FDP (Online Capacity Building Workshop) on “Blooming Research Areas Under IoT: A 360-Degree View” Organized by GFGCOE, Jalgaon during 02nd to 07th May 2023.
- One week International online FDP on “Block Chain Technology & it’s Applications” Organized by VMEG, Hyderabad during 25th to 29th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Artificial Intelligence 3.0” Organized by Pantech e Learning Pvt Ltd, during 24th April 2023 to 23th May 2023.
- 30 days APSSDC Sponsored Online FDP on “REACT JS” Organized by Pantech e Learning Pvt Ltd, during 10th April 2023 to 12th May 2023.
- 30 days APSSDC Sponsored Online FDP on “Embedded Systems” Organized by Pantech e Learning Pvt Ltd, during 27th March 2023 to 27th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Python Full Stack Development” Organized by Pantech e Learning Pvt Ltd, during 27th March 2023 to 27th April 2023.
- 30 days APSSDC Sponsored Online FDP on “Python Full Stack Development” Organized by Pantech e Learning Pvt Ltd, during 28th Feb 2023 to 29th March 2023.
- 30 days APSSDC Sponsored Online FDP on “Web Development using REACT JS” Organized by Pantech e Learning Pvt Ltd, during 04th January 2023 to 03rd February 2023.
- 30 days APSSDC Sponsored Online FDP on “Artificial Intelligence Master Class” Organized by Pantech e Learning Pvt Ltd, during 23rd November 2022 to 22nd December 2022.
- 30 days APSSDC Sponsored Online FDP on “Image Processing using OpenCV Master Class” Organized by Pantech e Learning Pvt Ltd, during 12th October 2022 to 11th November 2022.
- 30 days APSSDC Sponsored Online FDP on “Data Science 2.0 Master Class” Organized by Pantech e Learning Pvt Ltd, during 20th Sep 2022 to 21st Oct 2022.
- One week AICTE sponsored online FDP on “Amazon Web Services” Organized by KSRRM(A), Kadapa during 22nd to 27th August 2022.
- Five days National level online FDP on “Current Research Trends in Artificial Intelligence & Machine Learning” Organized by GIT, Gooty during 7th to 11th March 2022.
- Participated in a One week AICTE online sponsored STTP on “Practical Exposure on Machine Learning & Data Science (Phase-III)” Organized by GPREC (A), Kurnool during 13th to 18th September 2021.
- Participated in a One week AICTE online sponsored STTP on “Emerging Trends in Advanced Signal & Image Processing (Slot-3)” Organized by MRECW, Secunderabad during 19th to 24th April 2021.
- Participated in a One week AICTE online sponsored STTP on “Emerging Trends in Advanced Signal & Image Processing (Slot-1)” Organized by MRECW, Secunderabad during 15th to 20th April 2021.
- Participated in a One week AICTE online sponsored STTP(Phase-1) on “VLSI Design and Verification” Organized by AITS, Rajampet during 22nd to 27th March 2021.
- Participated in a One week AICTE sponsored online STTP on “Multi-Objective Optimization Methods for Engineering and Scientific Applications” Organized by SSTC, Bhilai during 29th January 2021 to 3rd February 2021.
- Participated in a One week AICTE sponsored online STTP on “Hands on Training in Artificial Intelligence & Optimization technique applications for Additional lab experiments and mini, major for BTech Engineering students” Organized by VJIT(A), Hyderabad during 07th December 2020 to 12th December 2020.

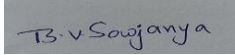
- Participated in a One week AICTE sponsored online FDP on “Quantum Computing and Machine Learning” Organized by JNTUACEP and Directorate of Faculty Development & IQAC, JNTU, Ananthapuramu during 30th November 2020 to 05th December 2020.
- Participated in a One week e-STTP on “Recent Trends in Instrumentation, Measurement & Automation” Organized by EG, Ajmer in Twinning with VJTI, Mumbai under TEQIP-3 during 26th October 2020 to 30th October 2020.
- Participated in a Five days ATAL Academy Sponsored online FDP on “Control Systems & Sensors Technology” Organized by Madan Mohan Malaviya University of Technology, Gorakhpur during 17th October 2020 to 21st October 2020.
- Participated in a 20 days APSSDC Sponsored Online FDP on “R Programming” Organized by 360DigiTMG during 07th Sep 2020 to 05th Oct 2020.
- Participated in a Three days Online FDP on “Enhancement of Professional Communication Skills” Organized by ALITS, Ananthapuramu during 20th Aug 2020 to 22nd Aug 2020.
- Participated in a Five days IEI Sponsored online FDP on “VLSI & Embedded Systems Design for Aerospace & Defence Applications” Organized by SVIST, Kadapa during 10th August 2020 to 14th August 2020.
- Participated in a One week online FDP on “Python Programming -II” Organized by ALITS, Ananthapuramu in association with Spoken Tutorial IIT, Mumbai during 03rd Aug 2020 to 08th Aug 2020.
- Participated in a One week online FDP on “Recent Advances in Embedded Systems” Organized by MGIT, Hyderabad during 07th August 2020 to 11th August 2020.
- Participated in a One week Online FDP on “Simulink: An Overview” Organized by DataTeach Labs during 25th June 2020 to 30th June 2020.
- Participated in a One week online FDP on “Introduction to Renewable Energy Sources” Organized by KITS, Guntur during 20th June 2020 to 24th June 2020.
- Participated in a Five days online FDP on “Research Trends in Image Processing” Organized by Gates Institute of Technology, Ananthapuramu during 15th June 2020 to 19th June 2020.
- Participated in a One day Webinar on “Design and Fabrication of Low Cost Microfluidic Device for Polymerase Chain Reaction(PCR)” Organized by Maratha Mandal Engineering College, Karnataka on 15th June 2020.
- Participated in a One week International online Knowledge Develop Program on “Challenges & Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View” Organized by GEC, Gulavalleru during 08th June 2020 to 13th June 2020.
- Participated in a One week online FDP on “Recent Trends & Research Areas in Applied VLSI and Advanced Communications” Organized by VVIT, Guntur during 08th June 2020 to 12th June 2020.
- Participated in a One week online FDP on “Recent Trends in Communication and Wireless Networks” Organized by KITS, Guntur during 5th June 2020 to 9th June 2020.
- Participated in a 2 weeks Online FDP on “Matlab Masters” Organized by DataTeach Labs during 08th June 2020 to 19th June 2020.
- Participated in a Two days APSSDC Sponsored Online FDP on “MATLAB and Simulink in Engineering Education” Organized by APSSDC during 10th June 2020 to 11th June 2020.
- Participated in a One day Webinar on “Free Space Optics” Organized by SVCE, Nellore on 09th June 2020.
- One day Webinar on “Simulation Driven Innovation for e-Mobility” Organized by ALTAIR, India on 09th June 2020.
- Participated in a One day Webinar on “Impact on Covid-19 on Engineering Education & A Way Forward” Organized by NRI Institute of Technology, Guntur in association with Efftronics on 08th June 2020.
- Participated in a One week online FDP on “Research Challenges & Emerging Trends in Electronics and Communication Engineering (RCEECE-2020)” Organized by Ramachandra College of Engineering, Eluru during 27th May 2020 to 01st June 2020.
- Participated in a 2 weeks Online FDP on “Matlab Tools & their Applications in Engineering” Organized by DataTeach Labs during 25th May 2020 to 07th June 2020.
- Participated in a 2 days Online FDP on “Essential Matlab for Engineers” Organized by DataTeach Labs during 29th May 2020 to 30th May 2020.

- Participated in a Three days Webinar FDP on “IoT with Artificial Intelligence” in Collaboration with 360DigiTMG Organized by Sir C R Reddy College of Engineering, Eluru during 27th May 2020 to 29rd May 2020.
- Participated in a Five days online FDP on “Recent Communication Tools and it’s Application” Organized by St. Martin’s Engineering College (A), Secunderabad during 19thMay 2020 to 23rdMay 2020.
- Participated in a Two days online Workshop on “VLSI Circuits Analog & Digital Design Perspectives” Organized by JNTU, Ananthapuramu in association with AMS Semiconductors & HCL Technologies during 16th May 2020 to 17th May 2020.
- Participated in a One day workshop on “Awareness on NPTEL Courses” conducted by IIT Madras at JNTUACollege of EngineeringPulivendula on 15th December 2018.
- Participated in a Six days workshop on “Embedded Systems (IOT)” conducted by APSSDC at JNTUACollege of EngineeringPulivendula from 21stto 26th September 2018.
- Participated in a four days Hands-on training on “**The Internet of Things (IOT) for digital world** ” organized by ESCI Hyderabad, at Tirupathi on 19th-22nd December 2016.
- Participated in a National Level workshop on “**Digital Image Processing, Pattern Recognition and its Applications**” organized by JNTUA college of engineering, Pulivendula on 19th-20th September 2016.
- Participated in a Two day National Level National Level workshop on “**VLSI Design Advancements using Xilinx Tools**” organized by JNTUA College of engineering, Pulivendula on 26th-27th August 2016.
- Participated in a two day National Level Hands-on Training Program on “**MSP 430 and TIVA Micro controllers**” organized by JNTUA College of engineering, Pulivendula on 23rd-24th July 2016.
- Participated in a Two days Hands-on “**Linear Integrated Circuits: A Systems Approach**” organized by JNTUA College of engineering, Pulivendula on 25th-26th September 2015.
- Participated in AICTE sponsred one week online short-Term training program on "**Practical Exposure on Machine Learning and Data Science**" organized by Department of ECE,GPREC(A),Kurnool during 13th september to 18th september 2021.

Declaration:

I hereby declare that the information furnished above is true to the best of knowledge

Date: 15-06-2023
Pulivendula


(B.V.SOWJANYA)